$O(\log\log n)$ Worst-Case Local Decoding and Update Efficiency for Data Compression

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***Abstract*—This paper addresses the problem of data compression with local decoding and local update. A compression scheme has worst-case local decoding $d_{\rm wc}$ if any bit of the raw file can be recovered by probing at most $d_{\rm wc}$ bits of the compressed sequence, and has update efficiency of $u_{\rm wc}$ if a single bit of the raw file can be updated by modifying at most $u_{\rm wc}$ bits of the compressed sequence. This article provides an entropy-achieving compression scheme for memoryless sources that simultaneously achieves $O(\log\log n)$ local decoding and update efficiency. Key to this achievability result is a novel succinct data structure for sparse sequences which allows efficient local decoding and local update.**

Under general assumptions on the local decoder and update algorithms, a converse result shows that the maximum of $d_{\rm wc}$ and $u_{\rm wc}$ must grow as $\Omega(\log\log n)$.

I. Introduction

Consider a source sequence X^n with independent and identically distributed (i.i.d.) components having probability mass function p_X on a finite alphabet $\mathcal{X}$. For simplicity we assume here that p_X is a known distribution over $\mathcal{X}=\{0,1\}$.[1]

A fixed-length compression scheme of rate R consists of a pair of algorithms, the encoder ENC and the decoder DEC. The encoder maintains for every X^n, a codeword $C^{nR}\in\{0,1\}^{nR}$ such that $\mathrm{DEC}(C^{nR})$ is a good estimate of X^n. The probability of error of the compression scheme is defined as

$$P_{\rm glob}\coloneqq \Pr[\mathrm{DEC}(C^{nR})\neq X^n].$$

From the source coding theorem, we know that there exist sequences of codes with rate arbitrarily close to the entropy $H(p_X)$ and error probability vanishing in n.

Our goal is to design a fixed-length compression scheme that additionally supports local encoding and decoding. A locally decodable and updatable compression scheme consists of a global encoder and decoder pair $(\mathrm{ENC},\mathrm{DEC})$ and in addition, a local decoder and a local updater:

- **Local decoder:** A local decoder is an algorithm which given $i\in[n]$, probes (possibly adaptively) a small number of bits of C^{nR} to output $\widehat{X}_i$. Here, $\widehat{X}_i$ is the ith symbol of $\widehat{X}^n\stackrel{\rm def}{=}\mathrm{DEC}(C^{nR})$. The worst-case local decodability $d_{\rm wc}$ of the scheme is the maximum number of bits probed by the local decoder for the worst-case i. The average local decodability d is the expected number of bits, averaged over the source distribution, probed to recover any $\widehat{X}_i$ for any i.
- **Local updater:** A local updater is an algorithm which given $i\in[n]$ and $\widetilde{X}_i\in\{0,1\}$, adaptively reads and modifies a small number of bits of C^{nR} to give $\widetilde{C}^{nR}$ such that $\widetilde{C}^{nR}=\mathrm{ENC}(X_1,\ldots,X_{i-1},\widetilde{X}_i,X_{i+1},\ldots,X_n)$. Here the modified symbol $\widetilde{X}_i$ is supposed to be distributed according to p_X and independent from X^n. Also, the local updater is assumed to have no prior knowledge of X^n or C^{nR} and, hence, must probe C^{nR} to obtain such information.

  The worst-case update efficiency $u_{\rm wc}$ is defined as the maximum of the sum of the bits read and written in order to update any i. Likewise, the average update efficiency u is the sum of the average number of bits probed and written in order to update any X_i.

We allow the local decoder and updater to be *adaptive*, in the sense that the next bit to be read/written can depend on the values of the bits read/written so far. If the locations to be probed/modified are independent of the realization of the message, then we say that the algorithm is *non-adaptive*.

It was recently shown in [1], [2] that $(d,u)=(O(1),O(1))$ is achievable. In that paper the authors also gave a separate compression scheme achieving

$$(d_{\rm wc},u)=(O(\log\log n),O(\log\log n)).$$

In particular, the question of whether

$$(d_{\rm wc},u_{\rm wc})=(O(\log\log n),O(\log\log n))$$

is achievable was left open. In this paper we answer this question in the affirmative. We also show that under certain additional assumptions on the local decoder and the local updater this locality is order optimal.

Our achievability proof is based on a novel succinct data structure for $O(b/\log b)$-sparse sequences of length b in the bitprobe model which for any $0<\delta<1$ takes space $O(\delta b)$ while enabling local decode and update using at most $O(\log b)$ and $O(\frac{1}{\delta}\log b)$ bit reads/writes respectively. Our restricted converse is based on an analysis of bipartite graphs that represent the encoding and decoding algorithms.

A. Prior work

Local decoding and update for entropy-achieving compression schemes have been studied mostly in isolation. The

[1]This can be generalized to the scenario where p_X is unknown to the encoder and decoder by first estimating p_X and then using this for designing the compression scheme as in [1]. Similarly, our results can be generalized to nonbinary alphabets.

problem of locally decodable source coding of random sequences has received attention very recently following [3], [4]. Mazumdar *et al.* [5] gave a fixed-length compressor of rate of $H(p_X) + \varepsilon$ with $d_{\rm wc}(1) = \Theta(\frac{1}{\varepsilon}\log\frac{1}{\varepsilon})$. They also provided a converse result for non-dyadic sources: $d_{\rm wc}(1) = \Omega(\log(1/\varepsilon))$ for any compression scheme that achieves rate $H(p_X) + \varepsilon$. Similar results are known for variable length compression [6] and universal compression of sources with memory [7]. Likewise, there are compressors that achieve [8] rate $R = H(p_X) + \varepsilon$ and update efficiency $u_{\rm wc} = O(1)$.

In the computer science community, the literature has mostly focused on the word-RAM model [9], [10], [11], [12], [13], [14], where each operation (read/write/arithmetic operations) is on words of size $w = O(\log n)$ bits each, and the complexity is measured in terms of the number of word operations required for local decoding/update. However, in this case the number of bitprobes required is $\Omega(\log n)$. For random messages, one can trivially achieve any rate $R > H(p_X)$ and local decoding/update efficiency of $O(\log n)$ bitprobes by partitioning the n message symbols into blocks of size $O(\log n)$ and compressing each block separately.

II. Contributions

Before we present our main results we make a few observations aimed at justifying our model, and in particular the requirements we impose on the local decoder and updater.

In general, the local decoder could produce an estimate $\widehat{X}_i^{(\rm loc)}$ which could be different from $\widehat{X}_i$ with the requirement that the probability of local decoding error

$$P_{\rm loc} \stackrel{\rm def}{=} \max_i \Pr[\widehat{X}_i^{(\rm loc)} \neq X_i]$$

should be small. However, we will only study schemes that satisfy the following property.

(A1) *Global encoding and decoding using local algorithms:* We assume that C^{nR} is obtained by running the local updater on each message symbol, and $\widehat{X}^n$ by running the local decoder for each bit. In other words, there is no separate global encoder or decoder.

Our primary motivation for assumption (A1) is that global decoding can be sped up using parallelization. If we have a large number of parallel processors (which grows with n), then the runtime of global decoding can be made sublinear in n. It must be noted that as a byproduct, the probability of local decoding error of any symbol is also equal to $o(1)$, hence we are (implicitly) demanding that $P_{\rm loc} = o(1)$. As we outline below, having separate local and global algorithms can potentially lead to trivial solutions.

1) No global decodability requirement: If we only require $P_{\rm loc}$ to be small without any constraints on the global decoding error $P_{\rm glob}$, then we can easily achieve any $R > H(p_X)$ and

$$(d_{\rm wc}, u_{\rm wc}) = \left(O\left(\log\frac{1}{P_{\rm loc}}\right), O\left(\log\frac{1}{P_{\rm loc}}\right)\right).$$

This can be obtained by partitioning the n-length message into blocks of size $b_0 = O(\log\frac{1}{P_{\rm loc}})$, and compressing each block using an entropy-achieving fixed-length compression scheme—notice that the probability of wrongly decoding any particular block vanishes exponentially with b_0. Hence, for any small but constant $P_{\rm loc} = \delta$ we can achieve

$$(d_{\rm wc}, u_{\rm wc}) = (O(1), O(1)).$$

2) Separate local and global decoders: Suppose that in addition to 1) we also want $P_{\rm glob} = o(1)$ using a separate global decoder to recover $\widehat{X}^n$. This can be obtained by using a low-density parity check (LDPC) code with $O(1)$ maximum variable and check node degrees. The codeword consists of two parts:

$$C^{nR} = (C^{n(R-\delta)}(1), C^{\delta n}(2)),$$

where $C^{n(R-\delta)}(1)$ is obtained as in the previous case by dividing the message into constant size b_0 blocks and separately encoding each, while $C^{\delta n}(2)$ is obtained as the syndrome (of the LDPC code) of the (Hamming) error vector between X^n and the decoding of $C^{n(R-\delta)}(1)$.

The local decoder only probes $C^{n(R-\delta)}(1)$, while the local updater needs to update both $C^{n(R-\delta)}(1)$ and $C^{n\delta}(2)$. Since we are using an LDPC code, $C^{n\delta}(2)$ can be updated using $O(1)$ bit modifications. Therefore, $(d_{\rm wc}, u_{\rm wc}) = (O(\log\frac{1}{P_{\rm loc}}), O(\log\frac{1}{P_{\rm loc}}))$.

The global decoder decodes both $C^{n(R-\delta)}(1)$ and $C^{\delta n}(2)$ and can recover X^n with $o(1)$ probability of error.

We will henceforth only analyze schemes that satisfy (A1). The main result of this article is the following:

Theorem II.1. *For any $\varepsilon > 0$ there exists a compression scheme for Bernoulli(p) sources that achieves*

$$(R, d_{\rm wc}, u_{\rm wc}) = \left(H(p) + \epsilon, O(\log\log n), O(\frac{1}{\epsilon}\log\log n)\right),$$

and the overall computational complexity of global encoding/decoding is quasilinear in n.

The above theorem is formally proved in Section III-A. Using the technique in [2, Appendix C], Theorem II.1 can also be extended to variable-length compression with zero error, under the relaxation that the local updater has oracle access to X^n, and $u_{\rm wc}$ only counts the number of codeword bits that need to be modified in order to effect a single update. The proof of the above theorem is based on a novel dynamic succinct data structure for sparse sequences that achieves $O(\log n)$ locality in the bitprobe model.

Lemma II.1. *Fix any $\delta > 0$. For every $\beta = o(b/\log b)$, there exists a dynamic succinct data structure for b-length binary vectors of sparsity at most β with the following properties. Any such vector occupies at most $\delta b(1 + o(1))$ bits, has worst-case local decoding $O(\log b)$ and worst-case update efficiency at most $O(\frac{1}{\delta}\log b)$.*

To prove a lower bound, we make two assumptions in addition to (A1):

(A2) *Independence to previous updates:* The local update function for the t'th update and the local decode function

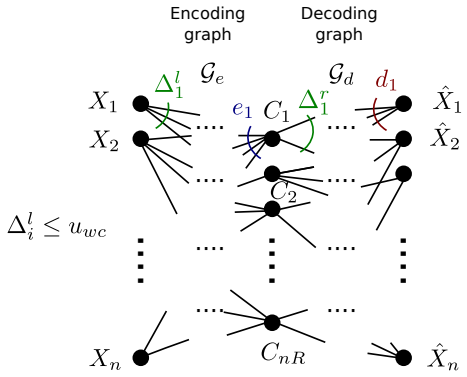

Fig. 1: Illustrating the encoding and decoding graphs under assumptions (A1) and (A2). The degree of the ith left vertex in $\mathcal{G}_e$ is Δ_i^l, while the jth right vertex is the local encodability of the ith symbol e_j, defined in Sec. IV. The degree of a right vertex in $\mathcal{G}_d$ is equal to the local decoding of the ith symbol d_i.

for the t'th local decode are deterministic functions of X^n, independent of t. In particular, conditioned on the realization of X^n at time t, they are both independent of the sequence of the previous $t-1$ updates.

Remark: Adaptive versus non-adaptive schemes: Under (A1) and (A2), any adaptive scheme achieving worst-case locality parameter l can be converted to a non-adaptive scheme with locality parameter $\leqslant 2^l$. This is because the adaptive scheme depends on at most l bits of its input, and there are at most 2^l possible configurations. The non-adaptive scheme can go through all possible configurations. We can therefore derive lower bounds for nonadaptive schemes, and any adaptive scheme must have locality that is at least logarithmic in this bound.

(A3) *Bounded average-to-worst case influence:* We assume that the number of message bits that influence a codeword bit (and number of decoded bits influenced by a codeword bit) on average is not too far from the worst case. To be more precise, for nonadaptive schemes, we can construct the local encoding graph $\mathcal{G}_e$ and the local decoding graph $\mathcal{G}_d$. Two vertices (i,j) in $\mathcal{G}_e$ are adjacent if C_j is a function of X_i. Likewise, (j,i) are adjacent in $\mathcal{G}_d$ if $\hat{X}_i$ is a function of C_j. See Fig. 1. We assume that the ratio of the average to the worst case degrees of the right vertices of $\mathcal{G}_e$ and left vertices of $\mathcal{G}_d$ are bounded from below by a constant independent of n.

Under (A1)–(A3),

Theorem II.2. *For any adaptive scheme with $R < 1$, we have*

$$d_{\mathrm{wc}} + u_{\mathrm{wc}} = \Omega(\log\log n).$$

As mentioned earlier, it is possible to achieve $(d_{\mathrm{wc}}, u_{\mathrm{wc}}) = (O(1), O(1))$ without assumptions (A1)–(A3). We believe that Theorem II.2 holds even without assumption (A3), but were unsuccessful in proving this. We also conjecture that it holds even without (A2). As we will see later, the construction of Lemma II.1 does not satisfy (A2).

III. Achievability

The high-level structure of our scheme is inspired by the locally decodable compressor in [5]: partition the set of message symbols into constant-sized blocks of $b = O(\log n)$ symbols each and use a fixed-length compressor for each block. The residual error vector is very sparse, which is encoded using a novel dynamic succinct data structure that allows local decode *and* update using only $O(\log b) = O(\log\log n)$ bitprobes.

In [5], the error vector was stored using the succinct data structure in [15] that allows local decoding of a single bit using $O(1)$ bitprobes. However, this data structure is static, in the sense that it does not allow efficient updates and hence does not get us small u_{wc}.

A well-known dynamic data structure in the word-RAM model is the van Emde Boas tree [16] which takes space $O(b)$ but allows local retrieval, insert and delete in $O(\log\log b)$ time (equivalently $O(\log b \log\log b)$ bitprobes). If we use the van Emde Boas tree for encoding the residual error vector, then we can achieve rate close to $H(p_X)$ but a higher locality of $O(\log\log n\ \mathrm{poly}(\log\log\log n))$.

A. Proof of Theorem II.1

We partition the n-length message sequence x^n into blocks $x^{b_1}(1), \ldots, x^{b_1}(n/b_1)$ of size $b_1 = O(\log n)$ each. Each block i is further partitioned into subblocks $(x^{b_0}(i,1), \ldots, x^{b_0}(i, b_1/b_0))$ of b_0 symbols each. Each subblock is compressed independently using a fixed-length lossy compression scheme of rate $H(p)+\epsilon$ and average per-letter distortion ϵ. Let $c^{b_0(H(p)+\epsilon)}(i,j)$ denote this subcodeword for the (i,j)th subblock. In addition, the error vectors (denoted $e^{b_0}(i,j)$ and equal to $x^{b_0}(i,j)$ if $x^{b_0}(i,j)$ is atypical and 0^{b_0} otherwise) are concatenated and for each i, $e^{b_1}(i) \stackrel{\text{def}}{=} (e^{b_0}(i,1), \ldots, e^{b_0}(i, b_1/b_0))$ is compressed using the scheme in Lemma II.1 to give $\bar{c}^{\epsilon b_1}(i)$ with $\delta = \epsilon$. If the sparsity of $e^{b_1}(i)$ is larger than $\alpha b_1/\log b_1$ for a suitably chosen $\alpha > 0$, then we say that an error has occurred.

We choose $b_1 = \alpha_1(\log n \log\log n)$ and $b_0 = \alpha_2(\log\log n)$.

Using Azuma's inequality (and carefully choosing α_1, α_2), the probability that the distortion in each block is greater than $\alpha \log n/\log\log n$ falls as $o(1/n)$.

The worst-case local decoding is at most $O(\log b_1) = O(\log\log n)$, while the worst-case update efficiency is $O(\frac{1}{\epsilon}\log\log n)$. The compression rate is $H(p)+2\epsilon$, and the overall probability of error (using the union bound over blocks) is $o(1)$. This completes the proof. □

All that remains is to prove Lemma II.1, which is is our main contribution.

B. A succinct data structure achieving $O(\log b)$ locality for sparse sequences of length b

The high-level idea in our data structure is to split the b symbols into chunks of $O(\log b)$ symbols each, and maintain a dynamic memory table where we only store the chunks with nonzero Hamming weight. Addressing is resolved by storing for each chunk a pointer which indicates the location in the memory table where the chunk is encoded.

We split the b-length sequence x^b into blocks of b_1 consecutive symbols each: $x^{b_1}(1), \ldots, x^{b_1}(b/b_1)$. The data structure consists of the following parts:

- Status bits: b/b_1 many bits $s_1, \ldots, s_{b/b_1}$, one for each block. The status bit s_i is set to 1 if the Hamming weight of $x^{b_1}(i)$ is greater than zero, and zero otherwise.
- Memory table: β many chunks $y^{b_m}(1), \ldots, y^{b_m}(\beta)$ of $b_m = b_1 + \log(b/b_1)$ bits each. The ith chunk $y^{b_m}(i)$ is split into two parts: $y^{b_1}(i,1)$ having b_1 bits, and $y^{\log(b/b_1)}(i,2)$ having $\log(b/b_1)$ bits. Here, $y^{b_m}(i,1)$ is a vector which stores a nonzero block $x^{b_1}(j)$ for some (suitably defined later) j, while $y^{\log(b/b_1)}(i,2)$ is a reverse pointer which encodes j in $\log(b/b_1)$ bits.
- Memory pointers: b/b_1 words $p^{b_p}(1), \ldots, p^{b_p}(b/b_1)$ of $b_p = \log\beta$ bits each, one for each block.
- Counter for number of nonzero blocks: $c^{\log\beta}$ is a vector of length $\log\beta$ bits which stores the number of nonzero blocks in x^b.

The overall codeword is a bit sequence obtained by the concatenation of the status bits, memory table, memory pointers and the counter. The total space required is

$$k_{ds} = \frac{b}{b_1} + \beta(b_1 + \log(b/b_1)) + \frac{b}{b_1}\log\beta + \log\beta \qquad (1)$$

1) Initial encoding: Let k denote the number of nonzero blocks in x^b.

- Status bits: If $x^{b_1}(i)$ has nonzero Hamming weight, then $s_i = 1$. Otherwise, it is set to zero.
- Memory pointers: If there are $j-1$ nonzero blocks among $x^{b_1}(1), \ldots, x^{b_1}(i-1)$ and $x^{b_p}(i)$ is nonzero, then $p^{b_p}(i) = j$ (or more precisely, the binary representation of j).
- Counter for number of nonzero blocks: $c^{\log\beta}$ is set to the number of nonzero blocks.
- Memory table: For every i, if $s_i = 1$ and $p^{b_p}(i) = j$, then the jth chunk contains information about $x^{b_1}(i)$. Specifically, $y^{b_m}(j,1) = x^{b_1}$, and $y^{\log(b/b_1)}(i,2)$ is equal to the binary representation of i.

2) Local decoding: Suppose that we want to recover x_i which happens to be the i_1th bit in the i_2th block.

- If $s_{i_2} = 0$, then output 0. This is because $s_{i_2} = 0$ implies that the entire block is zero.
- If not, then read $p^{b_p}(i_2)$. If $p^{b_p}(i_2) = j$, then output the i_1th bit in $y^{b_1}(j,1)$.

The maximum number of bits probed is

$$d_{\mathrm{wc}} = 1 + b_p + 1 = 2 + \log\beta.$$

3) Local update: Suppose that we want to update x_i (which happens to be the i_1th bit in the i_2th block) with $\widetilde{x}_i$. The update algorithm works as follows:

- Suppose that $x_i = 0$ and $\widetilde{x}_i = 1$. The updater first reads s_{i_2}.
  - If $s_{i_2} = 1$, then it reads $p^{b_p}(i_2)$. Suppose that $p^{b_p}(i_2) = j$. Then it writes $\widetilde{x}_i$ into the i_1th location of $y^{b_1}(j,1)$.
  - If $s_{i_2} = 0$, then it means that the block was originally a zero block. The updater sets s_{i_2} to 1, and increments the counter for the number of nonzero blocks $c^{\log\beta}$ by 1. Suppose that after incrementing, $c^{\log\beta} = j$. Then the updater sets $p^{b_p}(i_2) = j$, writes $\widetilde{x}^{b_1}(i_2)$ into $y^{b_1}(j,1)$, and sets $y^{\log(b/b_1)}(j,1)$ to i_2.
- Suppose that $x_i = 1$ and $\widetilde{x}_i = 0$. The updater first reads s_{i_2}. Clearly, this should be equal to 1. The updater reads $p^{b_p}(i_2)$ (suppose that it is equal to j), and then $y^{b_1}(j,1)$ to compute $x^{b_1}(i_2)$.
  - If $x^{b_1}(i_2)$ has Hamming weight greater than 1, then it flips the i_1th bit of $y^{b_1}(j,1)$.
  - If not, then it implies that $\widetilde{x}^{b_1}(i_2) = 0^{b_1}$. The updater next sets s_{i_2} to 0. It then decrements $c^{\log\beta}$. It next overwrites $y^{b_m}(j)$ with the contents of $y^{b_m}(c^{\log\beta})$, and sets $p^{b_p}(y^{\log(b/b_1)}(c^{\log\beta},2))$ to j. This is to consistently ensure that the first $c^{\log\beta}$ chunks of the memory table always contains all the information about nonzero blocks.

  The maximum number of bits that need to be read and written in order to update a single message bit is

$$u_{\mathrm{wc}} = 2 + b_p + 2\log\beta + 2b_m + b_p = 2 + b_1 + 4\log\beta + \log\frac{b}{b_1}.$$

4) Proof of Lemma II.1: Let us now prove the statement. We use the above scheme with $b_1 = O\left(\frac{1}{\delta}\log b\right)$. From (1), the total space used is $k_{ds} \leqslant \delta b(1 + o(1))$. The worst-case local decoding is equal to $O(\log b)$ and the worst-case update efficiency is equal to $O\left(\frac{1}{\delta}\log b\right)$. This completes the proof. □

Remark III.1. *The succinct data structure here satisfies (A1) but not (A2). Clearly, the order of the chunks in the memory table depends on the sequence of updates performed previously. For example, the first chunk could initially contain data of the first subblock. After a number of updates (e.g., involving setting all bits of the first block to zero, inserting bits in other blocks, and then repopulating the first block with ones), the first block could be stored in chunk $l > 1$.*

IV. Lower bounds on simultaneous locality

To obtain lower bounds, we introduce an additional parameter, the worst-case *local encodability*, e_{wc}, defined to be the maximum number of input symbols that any single codeword bit can depend on. Note that this is different from the update efficiency.[2] It has been established in the literature that separately, each of $d_{\mathrm{wc}}, u_{\mathrm{wc}}, e_{\mathrm{wc}}$ can be made $O(1)$ for near-entropy compression [5], [8], [17]. However, it is not known if $(d_{\mathrm{wc}}, u_{\mathrm{wc}}, e_{\mathrm{wc}}) = (\Theta(1), \Theta(1)l, \Theta(1))$ can be simultaneously achieved.

In this section, we assume (A1)–(A3) and that the scheme is nonadaptive.

1) The local encoding and decoding graphs:

- Under (A1) and (A2), the jth compressed bit C_j can be written as $C_j = f_j(X_{\mathcal{N}_e(j)})$ for some function f_j, where $\mathcal{N}_e(j)$ is the set of message locations that C_j can depend on. Clearly, $|\mathcal{N}_e(j)| \leqslant e_{\mathrm{wc}}$ for all j.

[2]This was studied in [17], where the authors related this quantity to a problem of semisupervised learning.

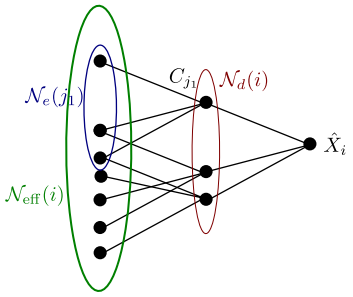

Fig. 2: Illustrating various neighbourhoods used in the proofs.

- The ith decoded bit $\hat{X}_i$ can be written as $\hat{X}_i = g_i(C_{\mathcal{N}_d(i)})$ for some function g_i, where $\mathcal{N}_d(i)$ is the set of codeword locations that need to be probed in order to recover X_i. Clearly, $|\mathcal{N}_d(i)| \leqslant d_{\text{wc}}$.
- We can construct an $n \times nR$ encoder bipartite graph $\mathcal{G}_e$ where (i, j) is an edge only if $i \in \mathcal{N}_e(j)$. This gives a natural lower bound on the update efficiency for the ith message symbol: it must be greater than or equal to the degree of the corresponding vertex in $\mathcal{G}_e$.
  The average degree of a vertex in the left (corresponding to a message symbol) is equal to R times the average degree of a right vertex (which corresponds to codeword bits). This implies that u_{wc} is lower bounded by the average (arithmetic mean of) the individual local encodabilities of the individual codeword bits.
- Similarly, we can construct the $nR \times n$ decoder bipartite graph where $(j, i) \in [nR] \times n$ is an edge if $j \in \mathcal{N}_d(i)$. The right degree of this graph is bounded from above by d_{wc}.
- Let $\mathcal{N}_{\text{eff}}(i) \stackrel{\text{def}}{=} \{X_l : l \in \bigcup_{j \in \mathcal{N}_d(i)} \mathcal{N}_e(j)\}$ denote the effective neighborhood. The ith decoded symbol is a function of only those symbols in $\mathcal{N}_{\text{eff}}(i)$, i.e., there exists a function h_i such that $\hat{X}_i = h_i(X_{\mathcal{N}_{\text{eff}}(i)})$.

See Fig. 1 and Fig. 2 for illustrations.

Let us now obtain a lower bound on the bit error probability of any compression scheme satisfying the above properties.

Lemma IV.1. *The probability of bit error, $P_e^{(i)} \stackrel{\text{def}}{=} \Pr[\hat{X}_i \neq X_i]$ satisfies*

$$P_e^{(i)} = \begin{cases} \geqslant (1-p)^{|\mathcal{N}_{\text{eff}}(i)|} \geqslant (1-p)^{e_{\text{wc}} d_{\text{wc}}}, & \text{or,} \\ 0. \end{cases}$$

Proof. For every $i \in [n]$, the decoded symbol $\hat{X}_i$ is a deterministic function (the composition of g_i and f_j's) of $\mathcal{N}_{\text{eff}}(i) \stackrel{\text{def}}{=} \{X_l : l \in \bigcup_{j \in \mathcal{N}_d(i)} \mathcal{N}_e(j)\}$. But we have $|\mathcal{N}_{\text{eff}}(i)| \leqslant e_{\text{wc}} d_{\text{wc}}$. The probability of error is given by[3]

$$\begin{aligned} P_e^i &= \sum_x \left(\prod_{l \in \mathcal{N}_{\text{eff}}(i)} \Pr[X_l = x_l] \right) 1_{\{X_i \neq \hat{X}_i\}} \\ &\geqslant \sum_x \left(\prod_{l \in \mathcal{N}_{\text{eff}}(i)} (1-p) \right) 1_{\{X_i \neq \hat{X}_i\}}. \end{aligned}$$

[3]For an event $\mathcal{E}$, $1_{\mathcal{E}}$ is the indicator function which takes value 1 if $\mathcal{E}$ occurs, and zero otherwise.

If there is even a single configuration of X^n for which $X_i \neq \hat{X}_i$, then $P_e^i \geqslant (1-p)^{|\mathcal{N}_{\text{eff}}(i)|} \geqslant (1-p)^{e_{\text{wc}} d_{\text{wc}}}$. □

Let $\eta_i \coloneqq |\mathcal{N}_{\text{eff}}(i)|$. Let u_i, e_j, d_i respectively denote the update efficiency for the ith message symbol, the local encodability of the jth codeword symbol, and the local decoding of the ith message symbol. These are also respectively greater than or equal to the degrees of the ith left vertex in $\mathcal{G}_e$, the jth right vertex in $\mathcal{G}_e$, and the ith right vertex in $\mathcal{G}_d$.

Lemma IV.2. *Consider any fixed-length compression scheme achieving vanishing probability of error and nontrivial compression rate $R < 1$. There exists a set $\mathcal{S} \subset [n]$ of message symbols with $\mathcal{S} = \Theta(n)$ such that for all $i \in \mathcal{S}$,*

$$\eta_i = \Omega(\log n).$$

Due to paucity of space, we only sketch the details. We select a subset $\mathcal{S}'$ of $[n]$ such that $P_e^i > 0$ for all $i \in \mathcal{S}'$. We know that $|\mathcal{S}'| \geqslant n(1-R)$. A more careful rederivation of Lemma IV.1 gives us $P_e^i \geqslant (1-p)^{\eta_i}$ for all $i \in \mathcal{S}'$. Hence,

$$\begin{aligned} P_e &\geqslant 1 - \prod_{i=1}^{n} (1 - P_e^i) && (2) \\ &\geqslant \sum_{i \in \mathcal{S}'} (1-p)^{\eta_i}, && (3) \end{aligned}$$

which is nonvanishing in n if $\eta_i < \log n$ for any subset of $\mathcal{S}'$ of size $n(1-R)/2$. Therefore, there must exist a subset $\mathcal{S} \subset \mathcal{S}'$ of size $n(1-R)/2$ where $\eta_i > \log n$ for all $i \in \mathcal{S}$. This completes the proof. □

This leads us to the following result:

Theorem IV.1. *Any fixed-length compression scheme achieving vanishing probability of error and $R < 1$, and satisfying assumptions (A1)—(A3) and nonadaptive local algorithms must have*

$$d_{\text{wc}} u_{\text{wc}} = \Omega(\log n).$$

Proof. From Lemma IV.2, there exists a set $\mathcal{S}$ of size $\Theta(n)$ such that for all $i \in \mathcal{S}$, $\eta_i = \Omega(\log n)$. However, $\eta_i \leqslant d_{\text{wc}} \sum_{j \in \mathcal{N}_d(i)} e_i$. From the graph $\mathcal{G}_e$, we also have

$$d_{\text{wc}} \sum_{j \in [nR]} e_i = d_{\text{wc}} \sum_{i \in [n]} \Delta_i^l \leqslant d_{\text{wc}} \sum_{i \in [n]} u_i \leqslant n d_{\text{wc}} u_{\text{wc}}, \quad (4)$$

where Δ_i^l denotes the degree of the ith left vertex in $\mathcal{G}_e$.

Using Lemma IV.2, we have

$$|\mathcal{S}| \Omega(\log n) \leqslant \sum_{i \in \mathcal{S}} \eta_i \leqslant \sum_{i=1}^{n} \eta_i \leqslant d_{\text{wc}} \sum_{j \in [nR]} e_j.$$

Using (4) in the above, and using the fact that $|\mathcal{S}| = \Theta(n)$, we have $n d_{\text{wc}} u_{\text{wc}} = \Omega(n \log n)$, which implies $d_{\text{wc}} u_{\text{wc}} = \Omega(\log n)$, completing the proof. □

Using Theorem IV.1 and our remark on adaptive vs nonadaptive schemes in Sec. II-2, we get Theorem II.2.

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